



IDS # 1204

PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO		<b>Complete if Known</b>			
		Application Number	10/629,733		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	July 30, 2003		
		First Named Inventor	H. CHAKIHARA, et al.		
		Art Unit	2826		
		Examiner Name	E. Abraham E. Ritz		
Sheet	1	of	1	Attorney Docket Number	501.42869X00

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
EP		6,740,921		05-2004	Matsuoka et al.	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>2</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)					

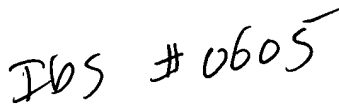
OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
EP		H. Takato, et al., "High Performance CMOS Surrounding Gate Transistor (SGT) for Ultra High Density LSIs", 1988 IEEE, pp 222-225	

Examiner Signature	Evan Ritz	Date Considered	12-20-06
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